

CONTROL DEVICES - MMSM PIN AND LIMITER DIODES RoHS Compliant

GENERAL DESCRIPTION

This series of surface mount PIN and Limiter diodes utilize new and unique monolithic MMSM technology. The technology is a package/device integration accomplished at the wafer fabrication level. Since the cathode and anode interconnections utilize precision photolithographic techniques rather than wire bonds, parasitic package inductance is tightly controlled. The package parasitics provide smooth non-resonant functionality through X Band. This series of devices meets RoHS requirements per EU Directive 2002/95/EC.

APPLICATIONS

The MPL4700 series of Limiter diodes are design for low to medium power receiver protection. The typical leakage output power for the MPL4701 is +20 dbm versus +24 dbm for the MPL4700. The MPL4702 is optimized for MRI surface coil applications.

Receiver protection circuitry frequently requires the antiparallel installation of 2 diodes. Microchip added antiparallel versions (-406 style) of our standard (-206 style) configurations. GC4702 and GC4703 – 406.

KEY FEATURES

- Up to 10W incident RF power handling
- Antiparallel configuration available
- Low parasitics
- L_P = 0.02nH Typical
- $C_P = 0.04pF$ Typical
- Broadband Performance through X-Band
- Available on Tape & Reel or on Film Frame for pick & place
- Small, SOD 323 Footprint
- RoHS Compliant ¹
- 1- These devices are supplied with gold terminations.

APPLICATION/BENEFITS

- · Receiver protection circuits
- Broadband Switching
- Economy Switching
- RF Attenuators
- MRI

ABSOLUTE MAXIMUM RATINGS @ 25°C

Rating	Symbol	Value	Unit
Maximum Leakage Current @80% of minimum Rated V _B	I _R	0.5	uA
Operating Temperature	Top	-55 to +150	°C
Storage Temperature	Tstg	-65 to +150	°C



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DEVICE ELECTRICAL PARAMETERS AT 25°C

Model Number	Package Style	V _B (V) I _R =10μΑ (Min)	C _τ (pF) ¹ @-10V (Max)	R _s (Ω) ² @1mA (Typ)	$\mathbf{R_s}(\Omega)^2$ @10mA (Max)	V _F If = 100μΑ (Min)
MPL4700	206	25	0.15	4	2.0ª	-
MPL4701	206	15	0.20	6	2.5ª	-
MPL4702	406	50 ^b	3.0°	12	2.0ª	0.45
MPL4703	406	25 ^b	0.3°	6	3.0 ^a	0.60

Model Number	Package Style	V _F If = 100mA (Max)	T∟ (Typ)	Application
MPL4700	206	-	20	Receiver Protection
MPL4701	206	-	10	Receiver Protection
MPL4702	406	1.0	30	Anti-parallel Pair MRI Surface Coil Detune
MPL4703	406	1.2	20	Receiver Protection

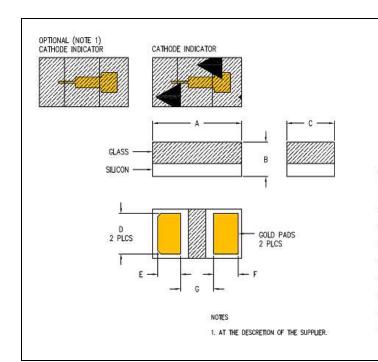
Notes:

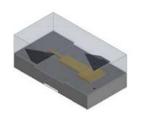
- 1- Capacitance is measured at f = 1 MHz.
- 2- Series Resistance (R_s) is measured at f = 100 MHz. Devices are mounted in a package suitable for testing.
 - a. Rs is measured at 1 GHz for the MPL series devices.
 - b. Not measured in anti-parallel configuration.
 - c. Vr = 0V. This value is the sum of two junctions.

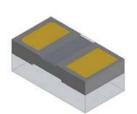


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PACKAGE STYLE 206





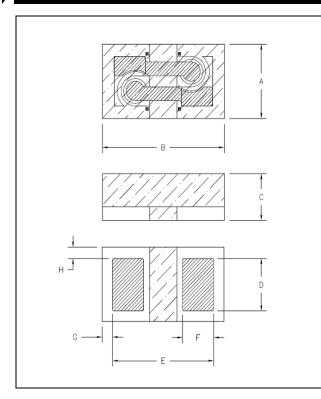


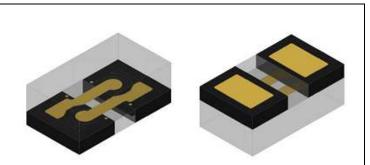
DIM IN		HES	M	М
DIM	MIN	MAX	MIN	MAX
Α	0.038	0.048	0.965	1.219
В	0.011	0.021	0.279	0.533
С	0.018	0.028	0.457	0.711
D	0.014	0.024	0.356	0.610
E	0.006	0.016	0.152	0.406
F	0.007	0.017	0.178	0.432
G	0.011	0.021	0.279	0.533



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PACKAGE STYLE 406



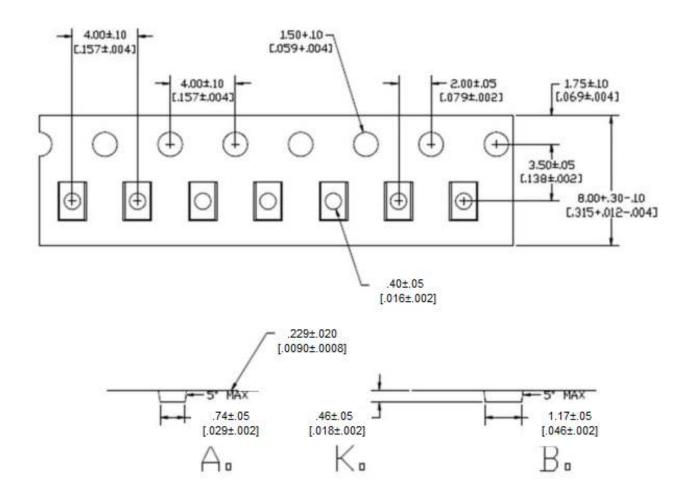


DIM MIN		INCHES		MM		
	MIN	TYP	MAX	MIN	TYP	MAX
Α	105	0.023	14-	===	0.584	33
В	-	0.043	04	=	1.092	==
C	0.014	1.5	0.020	0.356	==	0.508
D	4.0	0.019		=	0.483	
E	i e	0.039	12 5	5	0.991	5
F	92	0.012	19 <u>22</u>	<u>=</u>	0.305	<u> </u>
G	10 -1	0.002	ie.	-	0.051	÷
Н	622	0.002	629	25	0.051	25



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Tape and Reel Format-4 mm pitch



Notes:

- 1- For Waffle pack, use MPL47XX-206 or -406. For Tape and reel Format, use MPL47XX-206-HS or -406-HS.
- 2- 1000 or 5000 per reel dependent on customer

Revision History

Revision Level / Date	Para. Affected	Description
1 / 21 October 2013	-	Initial Release
2 / 17 November 2014	Application	Add Part# MPL4703 and Application notes
3 / 21 May 2020	- ` `	Add Tape & Reel Formatting
4 / 23 March 2021	Electrical Parameters	Changed MPL4703 V _F spec from 0.73 min to 0.60 min